

V. REMARKS

Claims 6-10 are rejected under 35 U.S.C. 102(e) as anticipated by Liang et al. (U.S. Patent Application Publication No. 2003/0016318). The rejection is respectfully traversed.

Liang discloses a color display achieving color images by controlling contrast of brightness of a plurality of pixels. Each of the pixels includes at least two sub-pixels. Each of the sub-pixels includes at least two colors. Each line of transversely-arranged sub-pixels is electrically connected together by a signal scan line. Each line of longitudinally-arranged colors is electrically connected together by a data transmission scan line. At least two of the data transmission lines connect the same color in each line of the longitudinally-arranged pixels being joined together by a conductive line to be connected to the same driving part.

Claim 6, as amended, is directed to an image display device provided in a gaming machine that includes a plurality of pixels arranged in a matrix extending in an x direction and a y direction being perpendicular to the x direction to form an xy plane. Claim 6 recites that each pixel includes a first pixel unit and a second pixel unit disposed adjacent the first pixel unit with each one of the first and second pixel units having a first pixel electrode operative to display a first color and a second pixel electrode operative to display a second color different from the first color and with the first and second pixel electrodes being arranged in a serial manner relative to each other in an identical manner for each pixel. Further, claim 6 recites that gate lines are wired in the y direction and connected to a scanning signal driver and information lines are wired in the x direction and connected to an information signal drive. Furthermore, claim 6 recites that the gate lines and the information lines are orthogonal in condition of being insulated mutually. Additionally, claim 6 recites that, when at least one pixel is energized, the same-color pixel electrodes of the at least one pixel are energized simultaneously.

It is respectfully submitted that the rejection is improper because the applied art fails to teach each and every element of claim 6, as amended. Specifically, it is respectfully submitted that the applied art fails to teach that gate lines are wired in the y direction and connected to a scanning signal driver, that information lines are wired in the x direction and connected to an information signal drive and that the gate lines and the information lines are orthogonal in condition of being insulated mutually.

Further, since the information signal lines wired in the X direction and connected to the information signal driver 10 and the gate lines wired in the y direction and connected to the scanning signal driver 11 are isolated mutually, signal delay caused by other voltage including noise etc. may be prevented.

Thus, it is respectfully submitted that claim 6 is allowable over the applied art.

Claim 8, as amended, is directed to a gaming machine that includes gate lines wired in the y direction and connected to a scanning signal driver, information lines wired in the x direction and connected to an information signal drive with the gate lines and the information lines being orthogonal in condition of being insulated mutually as recited in claim 6. Therefore, claim 8 is also allowable at least for the reasons is claim 6 is allowable.

Claim 7 depends from claim 6 and includes all of the features of claim 6. Thus, it is respectfully submitted that the dependent claim is allowable at least for the reasons claim 6 is allowable as well as for the features it recites.

Claims 9 and 10 depend from claim 8 and include all of the features of claim 8. Thus, it is respectfully submitted that the dependent claims are allowable at least for the reason claim 8 is allowable as well as for the features they recite.

Withdrawal of the rejection is respectfully requested.

Newly-added claims 11 and 12 also include features not shown in the applied art. Specifically, these features are pixel electrodes for the same color

included in a pair of pixel units a and b respectively are connected to a one-to-two relationship.

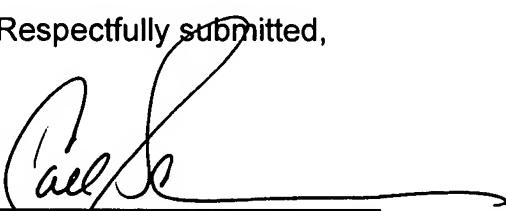
Further, Applicants assert that there are also reasons other than those set forth above why the pending claims are patentable. Applicants hereby reserve the right to submit those other reasons and to argue for the patentability of claims not explicitly addressed herein in future papers.

In view of the foregoing, reconsideration of the application and allowance of the pending claims are respectfully requested. Should the Examiner believe anything further is desirable in order to place the application in even better condition for allowance; the Examiner is invited to contact Applicants' representative at the telephone number listed below.

Should additional fees be necessary in connection with the filing of this paper or if a Petition for Extension of Time is required for timely acceptance of the same, the Commissioner is hereby authorized to charge Deposit Account No. 18-0013 for any such fees and Applicant(s) hereby petition for such extension of time.

Respectfully submitted,

By:


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Enclosure(s): Amendment Transmittal
 Petition for Extension of Time (three months)

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